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PROJECT INITIATION FORM
R&D LABORATORY

PROJECT TITLE: Transistorized Converter, 3-30 mcs (CV-5)

PROJECT NUMBER: 2512-7 NOMENCLATURE CV-5 PRIORITY II

EST. STARTING DATE 1 October 1958 DATE REQUIRED *None Specified

CLASSIFICATION/QUANTITY: EQUIPMENT Unclassified / 4 REPORT /

SOURCE OF REQUEST: O&T 58-804 dated 3 July 1958 DATE REC'D. 7-18-58

AUTHORITY: SS LIAISON CONTACT: [REDACTED] 25X1

RESPONSIBILITY: UNIT Design ENGINEER [REDACTED] 25X1

PROJECT DESCRIPTION:

Investigate the possibility of developing a 3-30 mc converter in a package about the size of the CV-2A. If feasible, 4 units should be packaged for delivery to O&T Division. Two additional units to be fabricated for Engineering Divisional use.

[REDACTED] indicates that the current scheduling which works this project on a time available basis is satisfactory. If the requirement changes he will notify us with as much lead time as possible.

Distribution: Design (2) ✓
Mechanical Fabrication
[REDACTED]
R&D
Lab

DOCUMENT NO. _____
NO CHANGE IN CLASS. ☒
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SIGNED [REDACTED]
DATE 15 September 1958

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REMARKS:

NCD _____

FROM:

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(47)

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Office Memorandum • UNITED STATES GOVERNMENT

TO : The Files

DATE: 28 July 1958

FROM :

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SUBJECT: Notes on CV-2A Program

1. The Stoddart variable capacitor used in the CV-2A has been redesigned by the manufacturer. The mounting of the capacitor has been changed in such a way as to make necessary redrawing and minor changes to the printed circuit board. The capacitor has been increased in capacity from 183 to 210 uuf in order to insure adequate frequency range tolerance at the ends of the bands.

2. A few fabrication problems came up during the manufacture of the prototype CV-2A. These problems, with the proposed solution, are listed below:

- a. Printed circuit board warpage sometimes making it difficult to insert the completed board in the chassis. Solution: Bake the boards to relieve stress and if necessary use a parallel stiffening board to relieve warpage. Spot weld flange on battery end cover.
- b. Hole tolerance between chassis and board too sloppy. Solution: Fabricate templates to insure accurate coincidence of holes in case to holes on printed circuit board. This procedure is now being followed where printed boards are used.
- c. The present battery holder is too long. The tolerances involved are such as to require insulation at the negative end of the battery holder. A shorter battery holder should be fabricated by the Lab which will do away with the insulating washers at the positive end; this will permit a saving of approximately 1/16" in the over-all length of the holder. Tighter hole tolerances on the board should also help alleviate this situation.
- d. It is suggested that 12 units of the CV-2A be fabricated along the above lines to take care of present commitments. If desired, these units may be tailored to a frequency range 4 - 16 mc.

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- e. Any additional requirements for a 3 to 30 mc converter which will require electronic re-design, breadboard test, and repackaging should be placed on a design basis aimed at prototype fabrication rather than the full-filment of present fabrication commitments.



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